

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Isaiah O. Oladeji, et al.

Serial No.: 10/699,975

Filed: November 2, 2003

For: MASK LAYER AND INTERCONNECT STRUCTURE FOR
DUAL DAMASCENE SEMICONDUCTOR MANUFACTURING

Group No.: 2827

Examiner: Maria F. Guerrero

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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August 9, 2006
Date of FilingElizabeth Schumacher
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Sir:

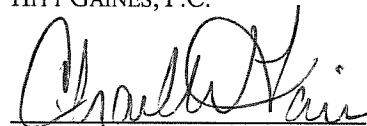
REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Attached is a copy of the Official Filing Receipt received from the Patent and Trademark Office in this application.

Please amend the Atty. Docket No. on the Official Filing Receipt to: JESSEN 12-4-6. Please note that a Revocation of POA/Change of Address/New POA has been filed on July 18, 2006 with the USPTO to correct the correspondence address to Customer Number 47396.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/699,975	11/02/2003	2827	770	Jessen 7-1-4/(284) Jessen 12-4-6	11	19	3

29391
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ORLANDO, FL 32801

CONFIRMATION NO. 5032

FILING RECEIPT



OC000000012753517

Date Mailed: 05/24/2004

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Applicant(s)

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Domestic Priority data as claimed by applicant

This application is a CON of 09/966,157 09/28/2001 ABN
and is a CON of 10/152,305 05/20/2002 ABN

Foreign Applications

If Required, Foreign Filing License Granted: 05/22/2004

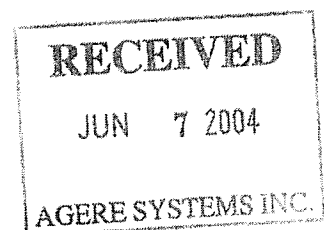
Projected Publication Date: 09/02/2004

Non-Publication Request: No

Early Publication Request: No

Title

Mask layer and interconnect structure for dual damascene semiconductor manufacturing



Preliminary Class

257

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